

Claims 2, 11 14 and 16 (marked with proposed changes):

✓ 2. (AMENDED) The method of claim 1, wherein said signal is applied to an input of ~~[an]~~ said circuit.

✓ 11. Apparatus for reducing distortion of a signal applied to an input of a circuit operating at high frequency and having a parasitic capacitance, comprising ~~[the steps of]~~ a detecting circuit for detecting a change in voltage of said input signal; and a correction circuit for changing an impedance of a parallel termination circuit that is in parallel with said parasitic capacitance to reduce distortion of said input signal.

14. Apparatus for reducing distortion of ^{an input} ~~a~~ signal applied to an input of a circuit operating at high frequency and having a parasitic capacitance, comprising: ^{at said input} ~~comprising:~~ a first circuit element for selectively providing current to said parasitic capacitance; a second circuit element for selectively preventing discharge of said parasitic capacitance ~~[into said input]~~ and a control circuit monitoring said input signal for respectively turning on said first circuit element and turning off said second circuit element when a positive going edge of said input signal is detected and for turning off said first circuit element and turning on said second circuit element when a negative going edge of said input signal is detected.

✓ 16. (AMENDED) Apparatus for reducing distortion of ^{an input} ~~a~~ signal applied to an input of a circuit operating at high frequency and having a parasitic capacitance, comprising: ^{at said input} ~~comprising:~~ a first circuit element for selectively providing current to said parasitic capacitance; a second circuit element for selectively preventing discharge of said parasitic capacitance ~~[into said input]~~ and a control circuit monitoring said input signal for respectively turning on said first circuit element and turning off said second circuit element when a positive going edge of said input signal is detected and for turning off said first circuit element and turning on said second circuit element when a negative going edge of said input signal is detected; said first and second circuit elements have a common terminal coupled to said parasitic capacitance; said first and second circuit elements being transistors.

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